

FIG. 1

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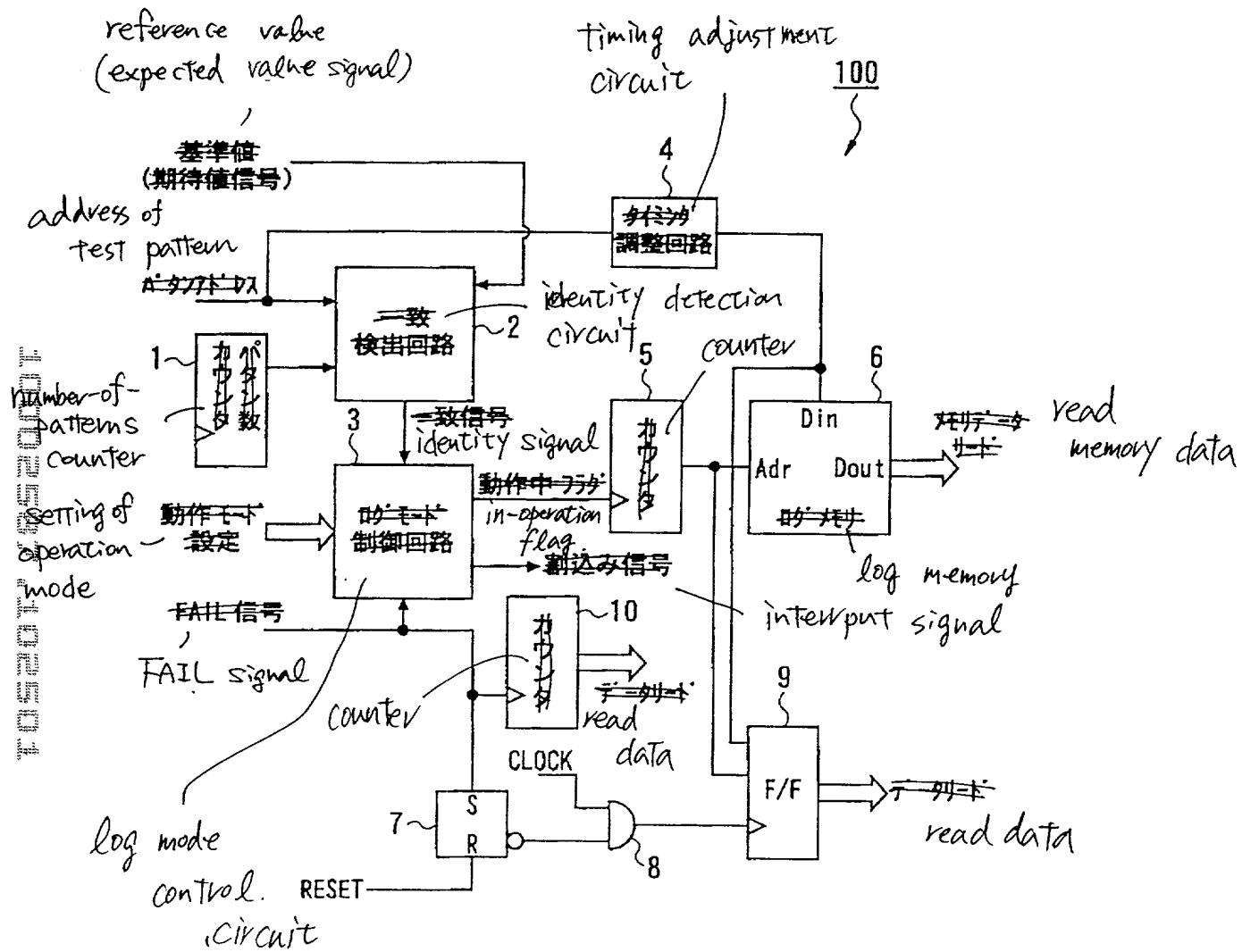


Fig. 2

リセット範囲  
reset range

to execute from address  
next to previously terminated  
address

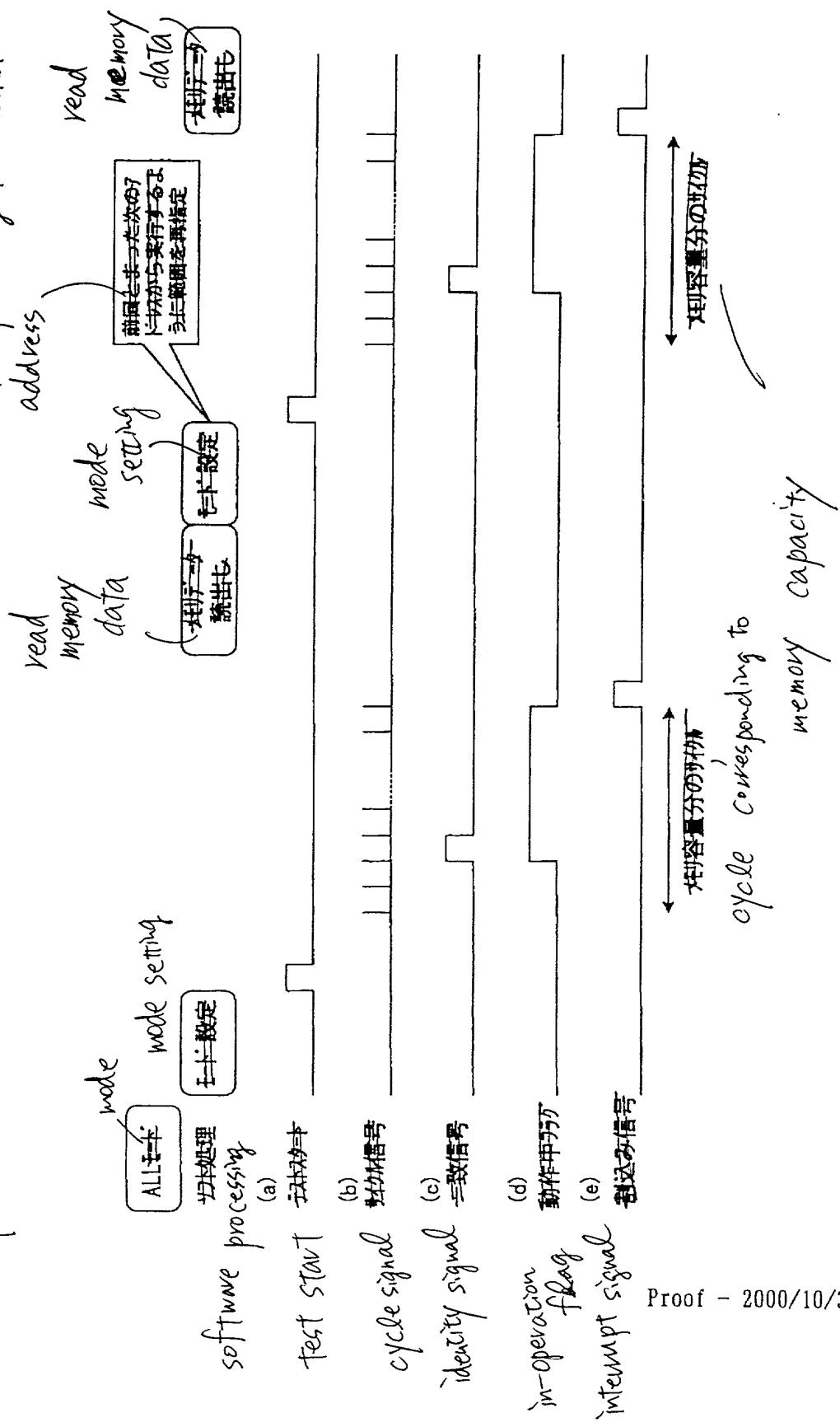


FIG. 3

range to be read. DATA can be omitted if a FAIL address is specified.

reset range to execute  
from address next to  
previousy terminated  
address : read FAIL

Address) read FAIL address  
memory data

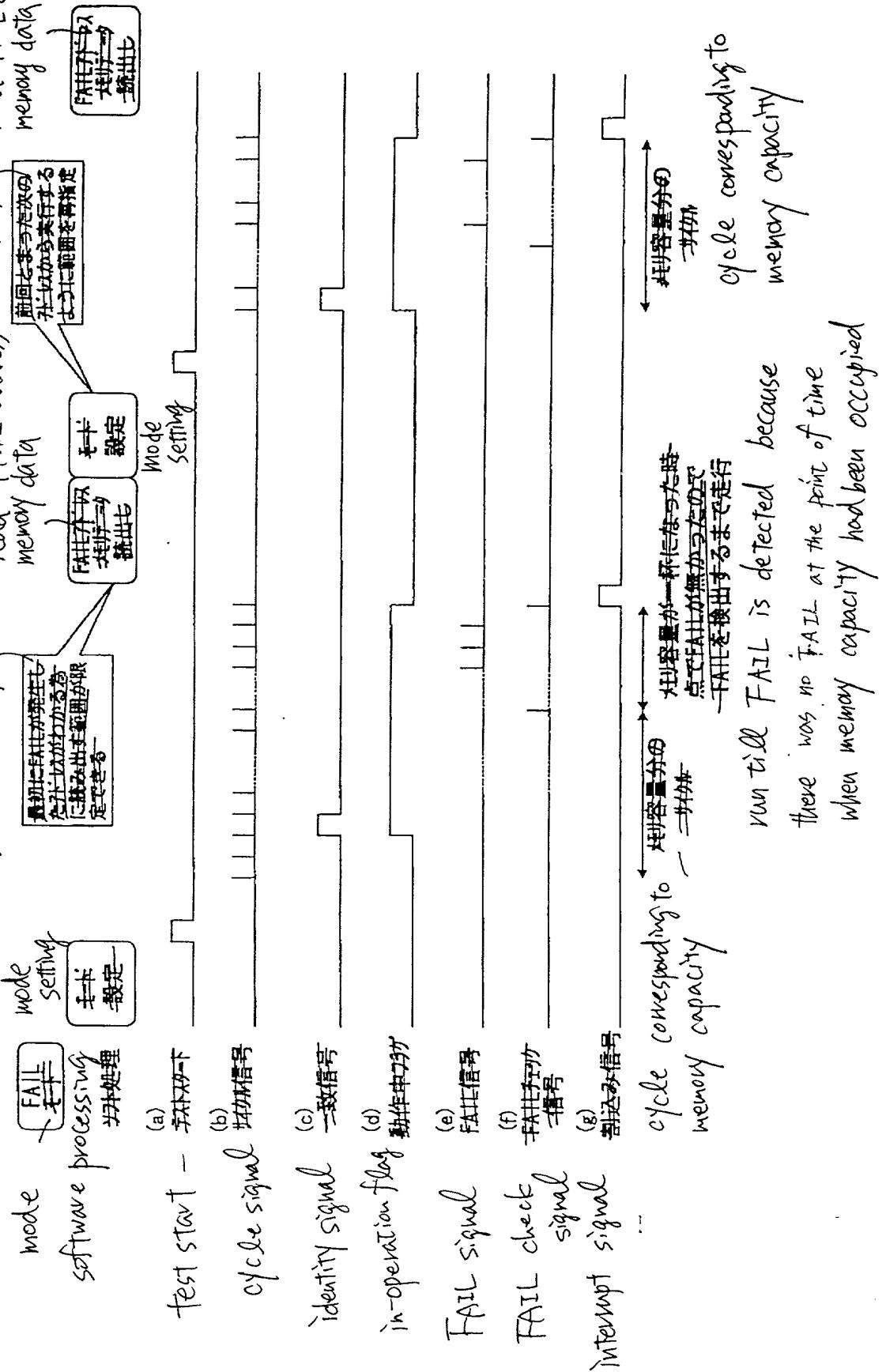
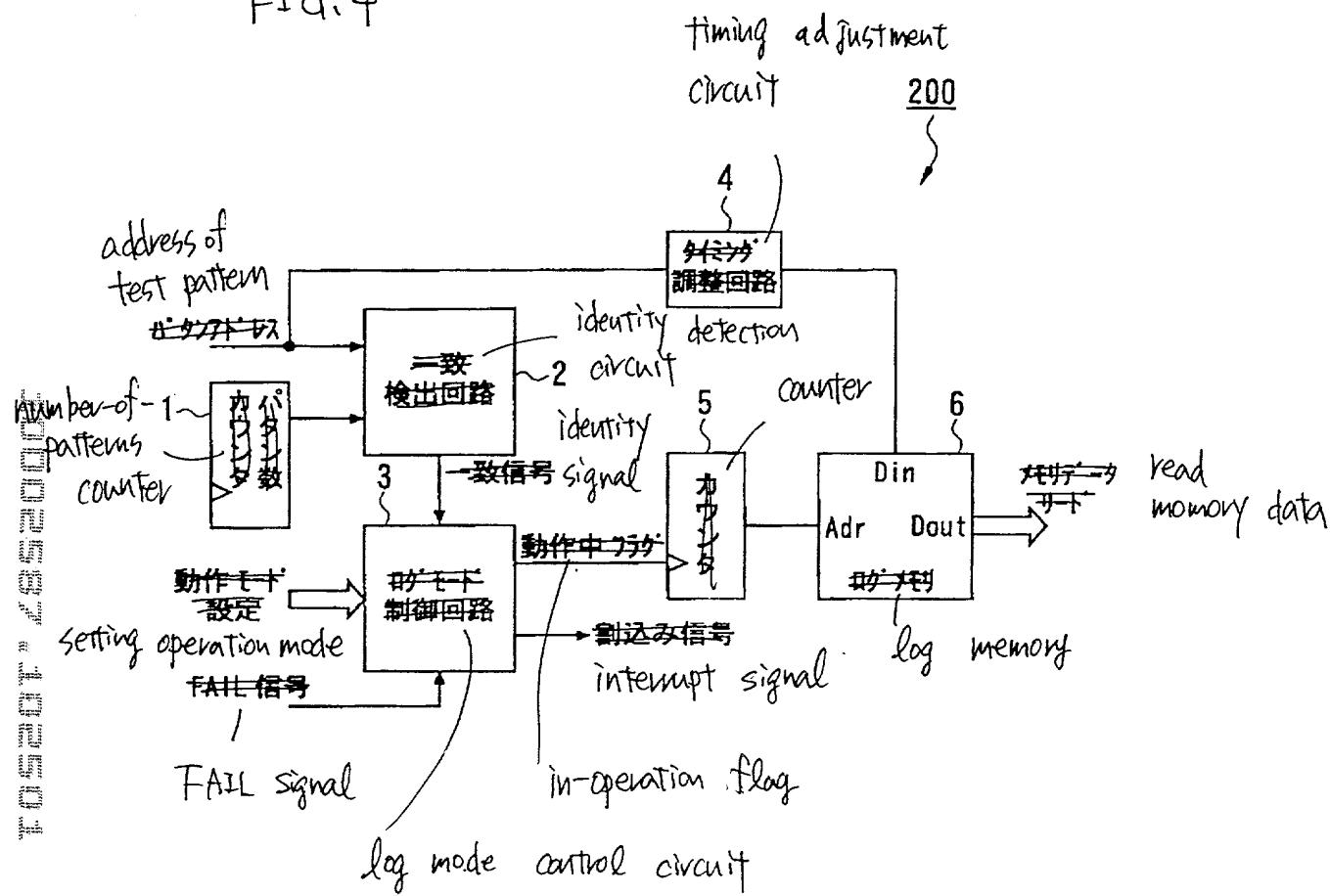


FIG. 4



TIG 5

Reset range to receive

From address next to  
previously terminated

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graph TD
    A[Address] --> B[read  
memory data]
    A --> C[write  
memory data]
    B --> D[七  
十  
設  
定]
    C --> E[七  
十  
統  
計]

```

前回七  
三十  
から操作する  
七  
十  
統  
計

mode setting  
モード設定  
software processing  
ソフト処理

(a) test start -   
 (b) cycle signal 

(c) 二进制信号

in-operation flag (d) 動作中フラグ

(e) FAIL signal

(f) ~~半掛車~~  
一體式

中断信号 (g) Interrupt signal

Proof - 2000/10/30

cycle corresponding to memory capacity / 短時間分の  
短時間分の 容量が一杯にならなかった  
点でFAILが無かったので  
FAILを検出するまで走行  
until FAIL is detected because  
cycle corresponding to memory capacity

there was no FAIL at the point of time when memory capacity had been occupied

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